

INVITATION

SEMINAR MINIMAL FAB

MAY 14TH 2019

Free admission
Novio Tech Campus, Nijmegen

DoMicro and BCSEMI NL invite you to the MinimalFAB Seminar.

On May 14th 2019, DoMicro and BCSEMI NL are organizing the 'MinimalFAB' seminar on the Novio Tech Campus in Nijmegen. Minimal FAB is a revolutionary new concept for semiconductor manufacturing developed by Dr. Hara, Principle Research Scientist at AIST, Japan. As part of his European tour, Dr. Hara will present the general concept and technology of the MinimalFAB and will discuss status, developments and opportunities. Discussion and networking will be part of the program.

Program

- 8:30** Walk in & coffee
- 9:00** Welcome by Mr. B. Peet, BCSEMI NL
- 9:10** Short introduction by Mr. M. Grooten, DoMicro
- 9:20** Short introduction by Tokyo Boeki
- 9:30** Minimal FAB; by Dr. Shiro Hara, AIST
 - general concept & technology
 - equipment and application
- 11:00** Coffee break
- 11:15** Half-inch FOWLP Process Line utilizing Minimal FAB; by Dr. Kenji Miyake, PMT Co.
- 12:00** Collaboration opportunities; by Mr. M. van Kooten, DoMicro
- 12:15** Discussion Q&A; All
- 12:45** Informal lunch buffet and networking
- 14:00** Closure

Venue



Novio Tech Campus

Building M
Transistorweg 5
6534 AT Nijmegen

Please note:

Most navigation systems do not recognize "Transistorweg 5". As an alternative you can use the address of the location "Jan Massinkhal" opposite to the entrance of Novio Tech Campus, building M, which is "Nieuwe Dukenburgseweg 5, Nijmegen".

Admission

free

REGISTER AND JOIN US

<https://www.eventbrite.nl/e/tickets-minimalfab-seminar-59438693733>

We warmly welcome you on May 14th in Nijmegen.



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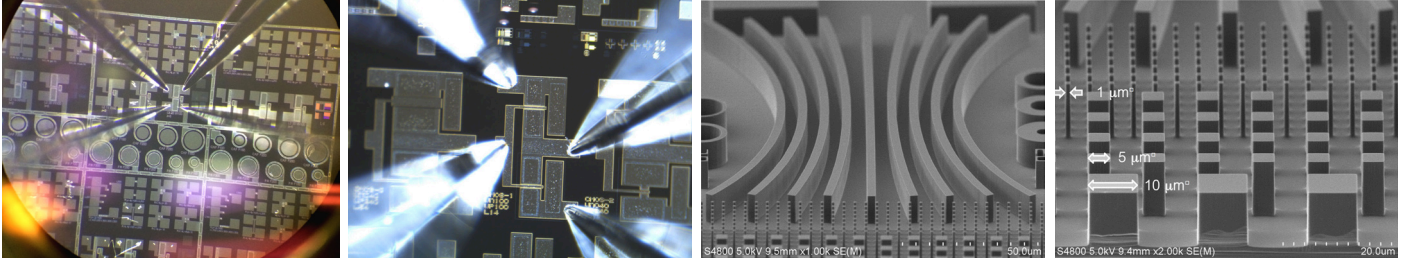
TOKYO BOEKI GROUP





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Dr. Hara



Dr. Shiro Hara received his Ph.D in engineering from Waseda University in Japan. In 1989, he joined Waseda University as Assistant Professor and special researcher, basic science program in Riken Institute since 1990.

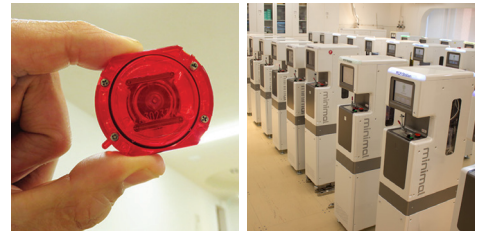
In 1993, he became a researcher of Electrotechnical Laboratory, MITI*. In 2007, he proposed the Minimal Fab concept to reduce an investment cost of a semiconductor factory into 1/1000.

During FY2012-2014, he promoted the national project on the development of minimal system as the project director. He is the Principal Research Scientist and the group leader of the minimal system group in AIST*, METI*. Also, he is the representative of Fab System Research Consortium of Minimal Fab Promoting Organization.

*AIST : National Institute of Advanced Industrial Science and Technology

*METI : Ministry of Economy, Trade and Industry

*MITI : former Ministry of International Trade and Industry



Dr. Kenji Miyake



Dr. Kenji Miyake received his Ph.D. degree in the Graduate School of Science and Engineering from Yamaguchi University in Japan.

He worked for Texas Instruments Japan for 28 years, since 1980. He conducted many international IT projects as Asia Pacific Assembly Automation Manager in Texas Instruments Japan.

He moved to PMT Corporation from Texas Instrument Japan in 2010. He has engaged in Minimal Foundry as Executive Officer, PMT Corporation.

He has contributed the international semiconductor symposiums that are AEC/APC (Advanced Equipment Control/Advanced Process Control) and ISSM (International Symposium of Semiconductor Manufacturing) as Program Committee since 2005.



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